

# Intel<sup>®</sup> Ethernet Network Adapter X710-DA2/DA4



Dual and Quad-port 10GbE adapters with Hardware Optimization and Offloads for the Rapid Provisioning of Networks in an Agile Data Center

#### **Key Features**

- PCI Express (PCIe) 3.0, x8
- Network Virtualization offloads including VxLAN, GENEVE, NVGRE, MPLS, and VxLAN-GPE with Network Service Headers (NSH)
- Intel® Ethernet Flow Director for hardware based application traffic steering
- Dynamic Device Personalization (DDP) enables increased packet processing efficiency for NFV and Cloud deployments
- Data Plane Development Kit (DPDK) optimized for efficient packet processing
- Excellent small packet performance for network appliances and Network Functions Virtualization (NFV)
- Intelligent offloads to enable high performance on servers with Intel® Xeon® processors
- I/O virtualization innovations for maximum performance in a virtualized server

#### Overview

The Intel® Ethernet Network Adapter X710 addresses the demanding needs of an agile data center by providing unmatched features for both server and network virtualization, flexibility for LAN and SAN networks, and proven, reliable performance.

The Intel® Ethernet 700 Series Network Adapters. These adapters are the foundation for server connectivity, providing broad interoperability, critical performance optimizations, and increased agility for Communications, Cloud, and Enterprise IT network solutions.

- Interoperability Multiple speeds and media types for broad compatibility backed by extensive testing and validation.
- Optimization Intelligent offloads and accelerators to unlock network performance in servers with Intel® Xeon® processors.
- Agility Both Kernel and Data Plane Development Kit (DPDK) drivers for scalable packet processing.

Intel® Ethernet 700 Series delivers networking performance across a wide range of network port speeds through intelligent offloads, sophisticated packet processing, and quality open source drivers.

# All Intel® Ethernet 700 Series Network Adapters include these feature-rich technologies:

### Flexible and Scalable I/O for Virtualized Infrastructures

Intel® Virtualization Technology (Intel® VT), delivers outstanding I/O performance in virtualized server environments.

I/O bottlenecks are reduced through intelligent offloads, enabling near-native performance and VM scalability. These offloads include Virtual Machine Device Queues (VMDq) and Flexible Port Partitioning using SR-IOV with a common Virtual Function driver for networking traffic per Virtual Machine (VM). Host-based features supported include:

VMDQ for Emulated Path: VMDQ, enables a hypervisor to represent a single network port as multiple network ports that can be assigned to the individual VMs. Traffic handling is offloaded to the network controller, delivering the benefits of port partitioning with little to no administrative overhead by the IT staff.

**SR-IOV for Direct Assignment:** Adapter-based isolation and switching for various virtual station instances enables optimal CPU usage in virtualized environments.

- Up to 128 virtual functions (VFs), each VF can support a unique and separate data path for I/O related functions within the PCI Express hierarchy.
- Use of SR-IOV with a networking device, for example, allows the bandwidth of a single port (function) to be partitioned into smaller slices that can be allocated to specific VMs or guests, via a standard interface.

Intel® Ethernet Adaptive Virtual Function (Intel® Ethernet AVF): Customers deploying mass-scale VMs or containers for their network infrastructure now have a common VF driver. This driver eases SR-IOV hardware upgrades or changes, preserves base-mode functionality in hardware and software, and supports an advanced set of features in the Intel® Ethernet 700 Series.

#### **Enhanced Network Virtualization Overlays (NVO)**

Network virtualization has changed the way networking is done in the data center, delivering accelerations across a wide range of tunneling methods.

VxLAN, GENEVE, NVGRE, MPLS, and VxLAN-GPE with NSH Offloads: These stateless offloads preserve application performance for overlay networks, and the network traffic can be distributed across CPU cores, increasing network throughput.

#### Flexible Port Partitioning (FPP)

FPP leverages the PCI-SIG SR-IOV specification. Virtual controllers can be used by the Linux host directly and/ or assigned to virtual machines.

- Assign up to 63 Linux host processes or virtual machines per port to virtual functions.
- Control the partitioning of per-port bandwidth across multiple dedicated network resources, ensuring balanced QoS by giving each assigned virtual controller equal access to the port's bandwidth.

Network administrators can also rate limit each of these services to control how much of the pipe is available to each process.

## Greater Intelligence and Performance for NFV and Cloud deployments

Dynamic Device Personalization (DDP) customizable packet filtering, along with enhanced Data Plane Development Kit (DPDK), support advanced packet forwarding and highly-efficient packet processing for both Cloud and Network Functions Virtualization (NFV) workloads.

- DDP enables workload-specific optimizations, using
  the programmable packet-processing pipeline.
  Additional protocols can be added to the default set
  to improve packet processing efficiency that results
  in higher throughput and reduced latency. New
  protocols can be added or modified on-demand and
  applied at runtime using Software Defined Firmware
  or APIs, eliminating the need to reset or reboot the
  server. This not only keeps the server and VMs up,
  running, and computing, it also increases
  performance for Virtual Network Functions (VNFs)
  that process network traffic that is not included in the
  default firmware. <u>Download DDP Profiles</u>
- DPDK provides a programming framework for Intel® processors and enables faster development of high-speed data packet networking applications.

#### **Advanced Traffic Steering**

Intel® Ethernet Flow Director (Intel® Ethernet FD) is an advanced traffic steering capability. Large numbers of flow affinity filters direct receive packets by their flows to queues for classification, load balancing, and matching between flows and CPU cores.

Steering traffic into specific queues can eliminate context switching required within the CPU. As a result, Intel® Ethernet FD significantly increases the number of transactions per second and reduces latency for cloud applications like memcached.

physical media.  Load balancing on multiple CPUs  Increases performance on multi-processor systems by efficiently balancing network loads across CPU core where used with Receive-Side Scaling (RSS) from Microsoft or scalable I/O on Linux.  Protect, Detect and Recover  The Intel Ethernet 700 Series implements a design philosophy of platform resiliency with 3 attributes supporting the NIST Cybersecurity Framework. Protect, Detect and Recover. These attributes verify the firmware and critical device settings with built-in corruption detection and automated device recovery to return the device to its originally programmed state.  Support for most network operating systems  Finables broad deployment for different applications.  Complies with the European Union directive 2011/65/EU to reduce the use of hazardous materials.  Finables networked Ethernet equipment to synchronize internal clocks according to a network master clock; endpoint can then acquire an accurate estimate of the master time by compensating for link latency.  I/O Features for Multi-Core Processor Servers  Intel® Ethernet Flow Director  An advanced traffic steering capability increases the number of transactions per second and reduces latency for cloud applications like Memcached.  MISH-X support  Niminizes the overhead of interrupts.  Load-balancing of interrupts.  Load-balancing of interrupts.  Actual number of queues will vary depending upon software implementation.  TX/RX IP, SCTP, TCP, and UDP checksum offloading (IPv4, IPv6) capabilities  Power processor usage.  Checksum and segmentation capability extended to new standard packet type.  Virtualization Features  Next-generation VMDq  Up to 256 maximum VMDq VMs supported.  Offloads the data-sorting based on MAC addresses and VLAN tags, functionality from the Hypervisor to the network silicon, improving data throughput and CPU usage.	Features	Description
Load balancing on multiple CPUs  - Increase performance on multi-processor systems by efficiently balancing network loads across CPU core where used with Receive-Side Scaling (RSS) from Microsoft or scalable (I) on Linux.  - Protect, Detect, and Recover  - The Intel Etherner ZOO Science implements a design philosophy of platform realizing, with a attributes supporting the NST Cybernecurity Framework Protect, Detect and Recover. These attributes verify the firmware and critical docks acting in the NST Cybernecurity Framework Protect, Detect and Recover. These attributes verify the firmware and critical docks acting in the busin incurreption detection and automated device recovery in the device to so originally programmod stats.  Support for most network operating systems  - Enables board deployment for different applications.  South-Compliant  - Compliant State (I) and the European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Compliant State Revolved Compliant and European Union directive 2011 [RS][EU to reduce the use of hazardous materials.  - Revolved Enhanced Play (I) (RS) (RS) (RS) (RS) (RS) (RS) (RS) (RS	General	
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the NST Cybenscurity Framework-Protect, Detect and Recover. These attributes verify the firmware and critical device settings with built-in corruption detection and summated device recovery to return the device to its inspirably importanced state.  Support for most network operating systems  Fonds-compliant  - Compliant  - Compl	Load balancing on multiple CPUs	• Increases performance on multi-processor systems by efficiently balancing network loads across CPU core when used with Receive-Side Scaling (RSS) from Microsoft or scalable I/O on Linux.
FoHS-compliant  Complies with the European Union directive 2011/65/EU to reduce the use of hazardous materials.  Final Sync (IEEE 1588, 802 1ss)  Final Environment Control of the Control of the Control of Cont	Protect, Detect and Recover	critical device settings with built-in corruption detection and automated device recovery to return the device to
Finables networked Ethernet equipment to synchronize internal clocks according to a network master clock; endpoint can then acquire an accordin editinate of the master time by compensating for link latency.    Vocation	Support for most network operating systems	Enables broad deployment for different applications.
### Indicates for Multi-Core Processor Servers	RoHS-compliant	• Complies with the European Union directive 2011/65/EU to reduce the use of hazardous materials.
Intel® Ethernet Flow Director (Intel® Tethernet Flow Director	Time Sync (IEEE 1588, 802.1as)	
Minimizes the overhead of interrupts.   Load applications like Memicached.	I/O Features for Multi-Core Processor	Servers
Load-balancing of interrupt handling between multiple cores/CPUs.   Multiple Queues: 1,536 Tx and Rx queues   Network packet handling without waiting for buffer overflow providing efficient packet prioritization.		• An advanced traffic steering capability increases the number of transactions per second and reduces latency for cloud applications like Memcached.
1.   1.   1.   1.   1.   1.   1.   1.	MSI-X support	
Virtualization Features		
Next-generation VMDq  - Up to 256 maximum VMDq VMs supported Offloads the data-sorting based on MAC addresses and VLAN tags, functionality from the Hypervisor to the network silicon, improving data throughput and CPU usage.  PCI-SIG SR-IOV Implementation (128 per device)  - Provides an implementation of the PCI-SIG standard for I/O Virtualization. The physical configuration of each point is divided into multiple virtual ports. Each virtual ports is assigned to an individual VM directly by bypassing the virtual source in the PCI-SIG standard for I/O Virtualization. The physical configuration of each point is divided into multiple virtual ports. Each virtual ports is assigned to an individual VM directly by bypassing the virtual source in the event of source, resulting in near-native performance.  - Integrated with Intel® YT for Directed I/O (Intel® YT-G) to provide data protection between VMs by assigning separate physical addresses in the memory to each VM 64/port for dual port.  - VIVILB provides traffic load balancing (Tx and Rx) across VMs bound to the team interface, as well as fault tolerance in the event of switch, port, cable, or adapter failure.  - 1536 eacht matched packets furnicas on multicast) 1512 hash entries each for unicast and multicast 1512 hash each tage each for unicast and multicast 1512 hash entries each for unicast and multicast 1513 hash entries each for uni		· · · ·
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tolerance in the event of switch, port, cable, or adapter failure.  Advanced Packet Filtering  - 1536 exact matched packets (unicast or multicast) 152 hash entries each for unicast and multicast Lower processor usage Promiscuous (unicast and multicast) Optional filtering of invalid frames.  VLAN support with VLAN tag insertion, stripping and packet filtering for up to 4096 VLAN tags  VXLAN, NVGRE, GENEVE, VXLAN-GPE+NSH, MPLS  - Preserves application performance in network virtualized environments.  Manageability Features  Preboot Execution Environment (PXE) Support - Enables system boot via the LAN (32-bit and 64-bit) Flash interface for PXE image.  Unified Extensible Firmware Interface (UEFI) - Enables new technologies during the pre-OS boot process and addresses legacy BIOS limitations on hardware.  Simple Network Management Protocol (SMMP) and Remote Network Monitoring (RMON) Statistic Counters  Watchdog Timer - Gives an indication to the manageability firmware or external devices that the controller or the software device driver is not functioning.  Specifications  General  Connections - Dual or Quad SFP+ cages supporting Direct Attach Copper (DAC) Twinaxial cable and optical transceivers  Network Standard Physical Layer Interfaces - 10GBASE-SR and -LR optical transceivers		virtual switch in the Hypervisor, resulting in near-native performance.  Integrated with Intel® VT for Directed I/O (Intel® VT-d) to provide data protection between VMs by assigning separate physical addresses in the memory to each VM.
	Virtual Machine Load Balancing (VLMB)	· · · · · · · · · · · · · · · · · · ·
VLAN support with VLAN tag insertion, stripping and packet filtering for up to 4096 VLAN tags  • Ability to create multiple VLAN segments.  • Preserves application performance in network virtualized environments.  • Preserves application performance in network virtualized environments.  • Enables system boot via the LAN (32-bit and 64-bit). • Flash interface for PXE image.  • Enables new technologies during the pre-OS boot process and addresses legacy BIOS limitations on hardware. • Easy system monitoring with industry-standard consoles.  • Easy system monitoring with industry-sta	Advanced Packet Filtering	<ul> <li>512 hash entries each for unicast and multicast.</li> <li>Lower processor usage.</li> <li>Promiscuous (unicast and multicast) transfer mode support.</li> </ul>
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Network Standard Physical Layer Interfaces 10GBASE-SR and -LR optical transceivers	General	
	Connections	Dual or Quad SFP+ cages supporting Direct Attach Copper (DAC) Twinaxial cable and optical transceivers
	Network Standard Physical Layer Interfaces	

Technical Features	
Operating Temperature	0 °C to 55 °C (32 °F to 131 °F)
Airflow	150 LFM with 55 °C required for CR (DAC) 150 LFM with 55 °C or 500 LFM with 65 °C required with extended temp SR optics
Storage Temperature	-40 °C to 70 °C (-40 °F to 158 °F)
Storage Humidity	Maximum: 90% non-condensing relative humidity at 35 °C
LED Indicators	LINK (solid) and ACTIVITY (blinking) LINK SPEED (green = 10Gbps; yellow = 1Gbps)

Adapter Features	
Data Rate Supported Per Port	Optical: 10/1GbE Direct Attach: 10GbE
Bus Type	PCIe 3.0 (8 GT/s)
Bus Width	PCIe x8
Interrupt Levels	INTA, MSI, MSI-X
Hardware Certifications	FCC A, UL, CE, VCCI, BSMI, CTICK, KCC
Controller	Intel® Ethernet Controller X710-BM2

Power Consumption		
SKU	Typical Power	Maximum Power
Dual-port 10GBASE-SR	4.3 W	4.8 W
Dual-port 1000GBASE-SX	4.0 W	4.3 W
Dual-port 10GBASE-LR	4.5 W	5.1 W
Dual-port Direct Attach (Twinax)	3.3 W	3.7 W
Quad-port 10GBASE-SR	6.2 W	6.6 W
Quad-port 1000GBASE-SX	5.5 W	6.0 W
Quad-port 10GBASE-LR	6.9 W	7.4 W
Quad-port Direct Attach (Twinax)	3.6 W	3.8 W

Physical Dimensions	
X710-DA2 Low profile	167 mm x 69 mm
X710-DA4 Low profile	167 mm x 69 mm

Product Order Code	
Configuration	Cisco Product ID
Dual Port	Server installed: UCSC-PCIE-ID10GF Spare adapter: UCSC-PCIE-ID10GF=
Quad Port	Server installed: UCSC-PCIE-IQ10GF Spare adapter: UCSC-PCIE-IQ10GF=
Cisco Servers Supported*	C220 M6, C240 M6, C225 M6, C245 M6, C220 M5, C240 M5, C480 M5, S3260 M5

\*Servers supported as of the date of this publication. For up-to-date server compatibility, please check: https://ucshcltool.cloudapps.cisco.com/public/

#### **Supported Operating Systems**

For a complete list of supported network operating systems for Intel® Ethernet 700 Series Adapters visit: intel.com/support/EthernetOS

#### Intel® Ethernet Optics

Combine proven, reliable Intel® Ethernet Optics with Intel Ethernet 700 Series Network Adapters for dependable interoperability and consistent performance across the network. Learn more at intel.com/ethernet

#### Supported Cisco Optics and Cables

These optics and cables are compatible for use with Intel® Ethernet Network Adapter X710. For the latest update, check the UCS Technical Specs, and consult Cisco Compatibility Matrix: https://tmgmatrix.cisco.com

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Cisco Product ID	Description
SFP-H10GB-CU1M	Cisco 10GBASE-CU SFP+ Cable 1 Meter, passive
SFP-H10GB-CU3M	Cisco 10GBASE-CU SFP+ Cable 3 Meter, passive
SFP-H10GB-CU5M	Cisco 10GBASE-CU SFP+ Cable 5 Meter, passive
SFP-10G-AOC7M	Cisco 10GBASE SFP+ Active Optical Cable, 7-meter
SFP-10G-SR-S	Cisco 10GBASE-SR SFP+ Module for MMF
SFP-10G-LR	Cisco 10GBASE-LR SFP+ Module for SMF
SFP-10G-SR	Cisco 10GBASE-SR SFP+ Module for MMF

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